

APPLICATION DATA SHEET

APPLICATION INFORMATION

Application Type::	REGULAR
Subject Matter::	UTILITY
CD-ROM or CD-R?::	NONE
Title::	SEMICONDUCTOR INTEGRATED CIRCUIT INCLUDING OPERATION TEST CIRCUIT AND OPERATION TEST METHOD THEREOF
Attorney Docket Number::	247421US2
Total Drawing Sheets::	9

INVENTOR INFORMATION

Applicant Authority Type::	INVENTOR
Status::	FULL CAPACITY
Given Name::	Yoshiyuki
Family Name::	TANAKA
Street of Mailing Address::	c/o Intellectual Property Division, Toshiba Corporation, 1-1, Shibaura 1-chome, Minato-ku,
City of Mailing Address::	Tokyo
Country of Mailing Address::	Japan
Applicant Authority Type::	INVENTOR
Status::	FULL CAPACITY
Given Name::	Yoshinari
Family Name::	OJIMA
Street of Mailing Address::	c/o Intellectual Property Division, Toshiba Corporation, 1-1, Shibaura 1-chome, Minato-ku,
City of Mailing Address::	Tokyo
Country of Mailing Address::	Japan

CORRESPONDENCE INFORMATION

Correspondence Customer Number::	22850
----------------------------------	-------

REPRESENTATIVE INFORMATION

Representative Customer Number:: 22850

FOREIGN PRIORITY INFORMATION

Application Number:	Country::	Filing Date::	Priority Claimed::
2003-001741	Japan	01/08/03	YES

ASSIGNMENT INFORMATION

Assignee Name:: KABUSHIKI KAISHA TOSHIBA
Street of Mailing Address:: 1-1, Shibaura, 1-chome,
Minato-ku,
City of Mailing Address:: Tokyo
Country of Mailing Address:: Japan